

<b>Notice of References Cited</b>	Application/Control No. 09/938,895		Applicant(s)/Patent Under Reexamination STEVENS, MAC	
	Examiner Anil Khatri		Art Unit 2124	Page 1 of 1

#### U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
X	A	US-5,592,600	01-1997	De Pauw et al.	345/440
X	B	US-6,678,876	01-2004	Stevens et al.	716/12
X	C	US-6,665,852	12-2003	Xing et al.	716/12
X	D	US-6,349,403	02-2002	Dutta et al.	716/12
X	E	US-6,275,956	08-2001	On et al.	717/125
X	F	US-5,862,381	01-1999	Advani et al.	717/125
X	G	US-6,014,507	01-2000	Fujii, Takashi	716/12
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

#### FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

#### NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)			
	U	Xing et al, "A minimum cost path search algorithm through tile onstacles", ACM ISPD April 2001, pp 192-197			
	V	Macii et al, "Combining wire swapping and spacing for low power deep submission buses", ACM GLSVLSI, April 2003, pp 198-202			
	W	Dion, "Fast printed circuit board routing", ACM/IEEE Design Automation Conf., 1987, pp 727-734			
	X				

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.